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	Application No.	Applicant(s)
Notice of Allowability	09/880,749	DRAPER, ANDREW M.
	Examiner	Art Unit
	JAMES C. KERVEROS	2133
The MAILING DATE of this communication app All claims being allowable, PROSECUTION ON THE MERITS IS herewith (or previously mailed), a Notice of Allowance (PTOL-85 NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT R of the Office or upon petition by the applicant. See 37 CFR 1.31	S (OR REMAINS) CLOSED in the or other appropriate communi RIGHTS. This application is sub-	nis application. If not included cation will be mailed in due course. THIS
1. This communication is responsive to RCE and Amendment	nt filed 3/21/2005.	
2. The allowed claim(s) is/are 1-23 and 25-29. Renumbered	as claims 1-28.	
3. $\boxtimes$ The drawings filed on <u>21 March 2005</u> are accepted by the	Examiner.	
<ul> <li>4. ☐ Acknowledgment is made of a claim for foreign priority u</li> <li>a) ☐ All b) ☐ Some* c) ☐ None of the:</li> <li>1. ☐ Certified copies of the priority documents have</li> </ul>		(f).
2.  Certified copies of the priority documents hav	e been received in Application I	No
3.  Copies of the certified copies of the priority do	ocuments have been received in	n this national stage application from the
International Bureau (PCT Rule 17.2(a)).		
* Certified copies not received:		
Applicant has THREE MONTHS FROM THE "MAILING DATE" noted below. Failure to timely comply will result in ABANDON! THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.		reply complying with the requirements
5. A SUBSTITUTE OATH OR DECLARATION must be subminformal PATENT APPLICATION (PTO-152) which give		
6. CORRECTED DRAWINGS ( as "replacement sheets") mu	ıst be submitted.	
(a) 🔲 including changes required by the Notice of Draftsper	son's Patent Drawing Review (	PTO-948) attached
1) 🗌 hereto or 2) 📗 to Paper No./Mail Date		
(b) ☐ including changes required by the attached Examiner Paper No./Mail Date	's Amendment / Comment or in	the Office action of
Identifying indicia such as the application number (see 37 CFR each sheet. Replacement sheet(s) should be labeled as such in		
7. DEPOSIT OF and/or INFORMATION about the deposit attached Examiner's comment regarding REQUIREMENT		
Attachment(c)		
Attachment(s) 1. ☐ Notice of References Cited (PTO-892)	5. Notice of Infor	mal Patent Application (PTO-152)
2. Notice of Draftperson's Patent Drawing Review (PTO-948)		
Information Disclosure Statements (PTO-1449 or PTO/SB/ Paper No./Mail Date		ail Date nendment/Comment
4. Examiner's Comment Regarding Requirement for Deposit	8. 🛛 Examiner's St	atement of Reasons for Allowance
of Biological Material	9. 🗌 Other	
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4	GUY LAMARRE PRIMARY EXAMINER	
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## NOTICE OF ALLOWANCE

## Continued Examination Under 37 CFR 1.114

A request for continued examination under 37 CFR 1.114, including the fee set forth in 37 CFR 1.17(e), was filed in this application after final rejection. Since this application is eligible for continued examination under 37 CFR 1.114, and the fee set forth in 37 CFR 1.17(e) has been timely paid, the finality of the previous Office action has been withdrawn pursuant to 37 CFR 1.114. Applicant's submission filed on 3/21/2005 has been entered.

This Office Action is a Notice of Allowance in response to RCE and Amendment filed 3/21/2005.

Claims 1, 11-15, 21, 25, 27 and 28 have been amended.

Claim 24 is cancelled.

The drawings were received on 3/21/2005. These drawings are acceptable.

Claims 1-23 and 25-29 are allowed. Renumbered as claims 1-28.

## **REASONS FOR ALLOWANCE**

The following is an examiner's statement of reasons for allowance:

The prior arts of record taken alone or in combination fail to teach, anticipate, suggest or render obvious the claimed invention, as follows:

Independent claim 1, as amended, recites inter alia, an embedded logic portion including a processor and a second JTAG circuit coupled to a first JTAG circuit and to the processor, wherein the second JTAG circuit includes TAP controller coupled to a

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second instruction register and to a second plurality of data registers, and at least some of the second plurality of data registers in the second JTAG circuit are designed to perform functions that are not performed by the first plurality of data registers.

Independent claim 11, as amended, recites inter alia, a second JTAG circuit having a second plurality of data registers, at least some of the first plurality of data registers being designed to perform functions not performed by the second plurality of data registers.

Independent claim 21, as amended, recites inter alia, performing a second set of functions using second data registers in the second JTAG circuit, wherein at least some of the second set of functions performed by the second data registers are different than the first set of functions performed by the first data registers.

Independent claim 27, as amended, recites inter alia, a multiplexer having a first input coupled to the first JTAG circuit and a second input coupled to the second JTAG circuit, wherein the second plurality of data registers are designed to perform functions that are not performed by the first plurality of data registers.

Consequently, independent claims 1, 11, 21 and 27 are allowed over the prior arts of record. Claims 2-10, 12-20 and 22, 23, 25 and 26 are directly or indirectly depended upon the independent claims and therefore are also allowable.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

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## Conclusion

Any inquiry concerning this communication or earlier communications from the examiner should be directed to JAMES C. KERVEROS whose telephone number is (571) 272-3824. The examiner can normally be reached on 9:00 AM TO 5:00 PM.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Albert Decady can be reached on (571) 272-3819. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

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Date: 25 April 2005 Office Action: Allowance JAMES C KERVEROS

Examiner

Art Unit 21

By:

GUY LAMARRE PRIMARY EXAMINE R